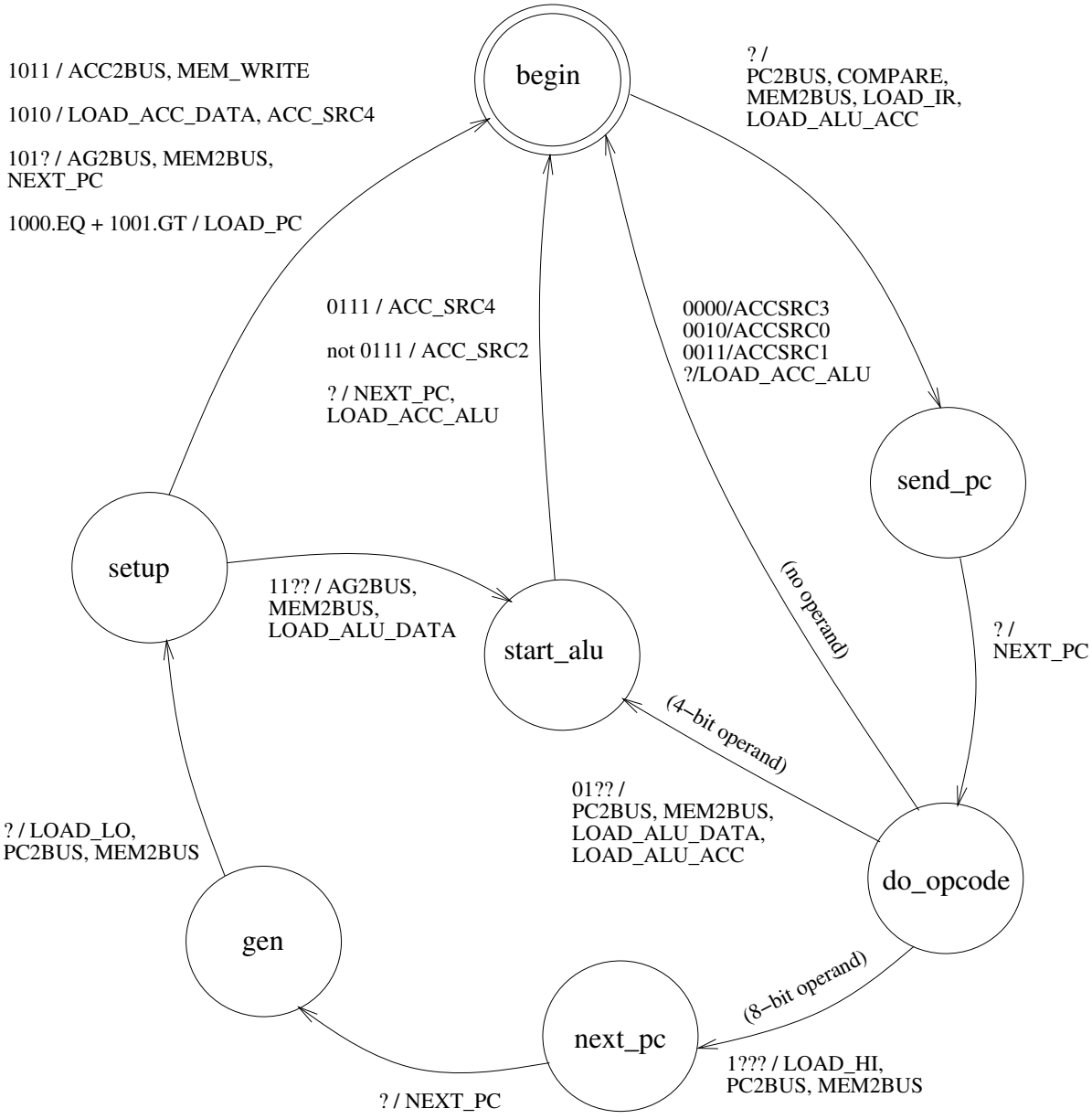
begin
1011 / ACC2BUS, MEM_WRITE
1010 / LOAD_ACC_DATA, ACC_SRC4
101? / AG2BUS, MEM2BUS, NEXT_PC
1000.EQ + 1001.GT / LOAD_PC
? / PC2BUS, COMPARE, MEM2BUS, LOAD_IR, LOAD_ALU_ACC
0111 / ACC_SRC4
not 0111 / ACC_SRC2
? / NEXT_PC, LOAD_ACC_ALU
0000/ACCSRC3
0010/ACCSRC0
0011/ACCSRC1
?/LOAD_ACC_ALU
send_pc
setup
11?? / AG2BUS, MEM2BUS, LOAD_ALU_DATA
start_alu
(no operand)
? / NEXT_PC
(4–bit operand)
01?? / PC2BUS, MEM2BUS, LOAD_ALU_DATA, LOAD_ALU_ACC
? / LOAD_LO, PC2BUS, MEM2BUS
do_opcode
gen
(8–bit operand)
next_pc
1??? / LOAD_HI, PC2BUS, MEM2BUS
? / NEXT_PC