

COMP 512
Rice University
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Operator Strength Reduction

— Generalities and the Cocke-Kennedy Algorithm —

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Consider the following simple loop



```
loadI
                                          \Rightarrow r_{sum}
             loadl 1
                                          \Rightarrow r_i
            loadl 100 \Rightarrow r_{100}
           subl r_i, 1 \Rightarrow r_1

multl r_1, 4 \Rightarrow r_2

addl r_2, @a \Rightarrow r_3 address

of a(i)
loop:
            load
                         r_3 \Rightarrow r_4
            \text{add} \quad r_{\text{4}}, r_{\text{sum}} \quad \Rightarrow r_{\text{sum}}
            addI
                         r_i, 1 \Rightarrow r_i
            cmp_LT r_i, r_{100} \Rightarrow r_5
                    r_5 \rightarrow loop, exit
            cbr
exit: ...
```

What's wrong with this picture?

- Takes 3 operations to compute the address of a(i)
- On some machines, integer multiply is slow



Consider the value sequences taken on by the various registers

```
loadI
                     0 \Rightarrow r_{sum}
         loadl 1 \Rightarrow r_i
                                                           r_{sum} = \bot
         loadl 100 \Rightarrow r_{100}
                                                           \mathbf{r}_{i} = \{ 1, 2, 3, 4, ... \}
loop: subl r_{i}, 1 \Rightarrow r_{1} r_{100} = \{ 100 \}
         multl r_1,4 \Rightarrow r_2 r_1 = \{0, 1, 2, 3, ...\}
         addl r_2,@a \Rightarrow r_3 r_2 = \{ 0, 4, 8, 12, ... \} load r_3 \Rightarrow r_4 r_3 = \{ @a, @a+4, @a+8, @a+12, ... \}
         add r_4, r_{sum} \Rightarrow r_{sum}
                                                          r_{1} = \bot
         addl r_i, 1 \Rightarrow r_i
         cmp_LT r_i, r_{100} \Rightarrow r_5
                                                         r_{5} = \bot
         cbr r_5 \rightarrow loop, exit
exit: ...
```

r_i , r_1 , r_2 , and r_3 take on predictable sequences of values

- r₁ and r₂ are intermediate values, while r₃ and r_i play important roles
- We can compute them cheaply & directly



Computing r₃ directly yields the following code

```
loadI
                              \Rightarrow r_{sum}
         loadI 1
                              \Rightarrow r_i
                                                             address of a(i)
         loadl 100 \Rightarrow r_{100}
         loadl @a \Rightarrow r<sub>3</sub>
        load r_3 \Rightarrow r_4
loop:
         addl r_3, 4 \Rightarrow r_3
                                                         r_3 = \{ @a, @a+4, @a+8, @a+12, ... \}
         add r_4, r_{sum} \Rightarrow r_{sum}
         addl r_i, 1 \Rightarrow r_i
         cmp_LT r_i, r_{100} \Rightarrow r_5
         cbr r_5 \rightarrow loop, exit
                                                         Still, we can do better ...
exit: ...
```

- From 8 operations in the loop to 6 operations
- No expensive multiply, just cheap adds



Shifting the loop's exit test from r_i to r_3 yields

- Address computation went from -,+,* to +
- Exit test went from +, cmp to cmp
- Loop body went from 8 operations to 5 operations
 - ♦ Got rid of that expensive multiply, too

Pretty good speedup on most machines

37.5% of ops in the loop, even if mult takes one cycle

Not redundant or invariant



And, as an aside, unrolling also helps

Now, 8 operations for 2 iterations, or 50% of the operations and a smaller percentage of the cycles (due to elimination of multiplies)

Opportunities



Operator Strength Reduction

- Transformed code has lots of address arithmetic
- With wrong shape, it has 9 or 10 induction variables, each needing a register
- Another version of this loop has
 33 or more potential induction variables

49 continue

do 50 i = nextra+1, n1, 4

$$y(i) = y(i) + x(j) * m(i,j)$$

 $+ x(j+1) * m(i,j+1)$
 $+ x(j+2) * m(i,j+2)$
 $+ x(j+3) * m(i,j+3)$

50 continue

60 continue

One of several hand-optimized versions of the loop

Opportunities



Operator Strength Reduction

```
subroutine dmxpy (n1, y, n2, ldm, x, m)
                                                         The largest version of the hand-
double precision y(*), x(*), m(ldm,*)
                                                         optimized loop in dmxpy.
jmin = j+16
do 60 j = jmin, n2, 16
   do 50 i = 1, n1
      + x(j-15)*m(i,j-15)) + x(j-14)*m(i,j-14)) + x(j-13)*m(i,j-13))
          + x(j-12)*m(i,j-12)) + x(j-11)*m(i,j-11)) + x(j-10)*m(i,j-10))
                                                                              33 distinct
          + x(j-9)*m(i,j-9)) + x(j-8)*m(i,j-8)) + x(j-7)*m(i,j-7))
                                                                              addresses
                                                                              (+ i \& i)
          + x(j-6)*m(i,j-6)) + x(j-5)*m(i,j-5)) + x(j-4)*m(i,j-4))
          + x(j-3)*m(i,j-3)) + x(j-2)*m(i,j-2)) + x(j-1)*m(i,j-1))
           + x(j) *m(i,j)
     continue
50
60 continue
   end
```

Opportunities



Operator Strength Reduction

- A reference, such as V[i], translates into an address expression
 @V₀ + (i-low) * w
- A loop with references to V[i], V[i+1], & V[i-1] generates

$$@V_0 + (i-low) * w$$

 $@V_0 + (i-(low-1)) * w$
 $@V_0 + (i-(low+1) * w$

Assumptions:

V is declared V[low:high]. Elements are w bytes wide. Constants have been folded.

- OSR may create distinct induction variables for these expressions, or it may create one common induction variable
 - ♦ Matter of code shape in the expression
 - ◆ Difference between 33 induction variables in the dmxpy loop and one or two
- Situation gets more complex with multi-dimensional arrays



Definition

Operator Strength Reduction is a transformation that replaces a strong (expensive) operator with a weaker (cheaper) operator

Strong form

Replace series of multiplies with adds

Weak form

The Problem

Replace single multiply with shifts and/or adds

Replace single multiply with silits and/or adds

• Its easy to see the transformation

Its somewhat harder to automate the process

See, for example, Lefevre's paper on the class web site.



The Cocke-Kennedy Algorithm

To explain strength reduction, we will begin with the multi-pass Cocke-Kennedy algorithm.

Assumptions

- Intermediate representation is low-level, **ILOC**-like code
- Have already built a control-flow graph (cfg)
- Have found either "natural loops" or "strongly connected regions" (SCRs)
- Have added a landing pad to each region

Definitions

- A region constant (RC) is a variable whose value is unchanged in the SCR
- An induction variable (IV) is a variable whose value changes in the SCR only by operations that increment or decrement it by an RC or an IV.

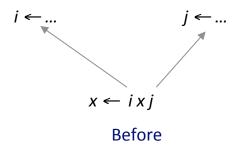
J. Cocke and K. Kennedy, "An Algorithm for Reduction of Operator Strength," *Communications f the ACM* 20(11), Nov. 1977, pages 850 – 856.

益益

The Cocke-Kennedy Algorithm

The Problem

- Easy to apply transformation by hand *
- Difficult to automate the process



The Big Picture

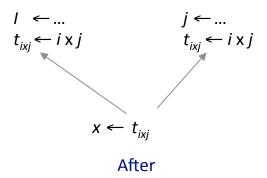
- Find induction variables and their uses
- Introduce a new induction variable tailored to each use
 - ◆ Requires both an initialization & appropriate updates
- Shift remaining uses from original induction variables to new ones
- Eliminate the original induction variables from the code

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The Cocke-Kennedy Algorithm

The Problem

- Easy to apply transformation by hand
- Difficult to automate the process



The Big Picture

- Find induction variables and their uses
- Introduce a new induction variable tailored to each use
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The Cocke-Kennedy Algorithm

The Problem

- Easy to apply transformation by hand
- Difficult to automate the process

The Algorithmic Plan

passes over the IR

A large number of

- 1. Find loops in the control-flow graph
- 2. Find *region constants* for those loops
- 3. Find induction variables
- 4. Find operations that are candidates to be reduced
- 5. Find all the values that affect the uses in *candidate* operations
- 6. Perform the actual replacement
- 7. Rewrite end-of-loop tests onto newly introduced induction variables
- 8. Dead-code elimination

"Linear function test replacement"

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The Cocke-Kennedy Algorithm

Step 1: Find *loops* in the **CFG** as **SCR**s

Apply Tarjan's strongly-connected region finder to the CFG

See R.E. Tarjan, "Depth-first search and linear graph algorithms," SIAM Journal of Computing, 1(2), 1972 pages 146 - 160

→ This algorithm is also the basis for next lecture, on the Vick-Simpson **OSR** algorithm, so you should read the paper if you haven't already done so

Step 2: Find *region constants* in the loops

Assume that we have performed loop-invariant code motion first.¹
Any value that is used in the SCR and not defined in the SCR is in RC
For each SCR, build a set of names that are defined (DEF) and a set of names that are used (USE). (linear pass over blocks in the SCR)

Then, RC is just (USE - DEF) or (USE ∩ NOT(DEF))

¹ If not, the test for region constant must also consider a variable that is assigned the same value along different paths through the **SCR**. In practice, it is easier to perform something like **LCM** first.

An induction variable is only updated by an add, subtract, copy, or negation involving induction variables and region constants



The Cocke-Kennedy Algorithm

Step 3: Find Induction Variables Assumes **SCR**s and **RC**s

```
for each op o (t \leftarrowo<sub>1</sub> op o<sub>2</sub>) in the SCR do if op \in { ADD, SUB, NEG, COPY} IV \leftarrow IV \cup { t } changed \leftarrow true while (changed) changed \leftarrow false for each operation o where t \in IV if o<sub>1</sub> \notin (IV \cup RC) or o<sub>2</sub> \notin (IV \cup RC) remove t from IV changed \leftarrow true
```

Simple fixed-point algorithm Applied to each **SCR**

For exposition, a candidate is a multiply than can be reduced.



The Cocke-Kennedy Algorithm

Step 4: Find operations that are candidates to be reduced Assumes **SCR**s, **IV**, and **RC**

```
\begin{aligned} & \textbf{CANDIDATES} \leftarrow \emptyset \\ & \text{for each op o } (t \leftarrow o_1 \text{ op o2}) \text{ do} \\ & \text{if op is a MULTIPLY then} \\ & \text{if } (o_1 \in \textbf{IV} \text{ and } o_2 \in \textbf{RC}) \text{ or } (o_1 \in \textbf{RC} \text{ and } o_2 \in \textbf{IV}) \\ & \text{then CANDIDATES} \leftarrow \textbf{CANDIDATES} \cup \{o\} \end{aligned} \qquad \qquad \begin{aligned} & \textbf{Applied to} \\ & \text{each SCR} \end{aligned}
```

CANDIDATES contains all multiplies that involve exactly one **RC** and one **IV**To expand the algorithm to other reductions, expand the test for candidates

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The Cocke-Kennedy Algorithm

Naming

- Create a new name for each unique candidate expression (hash them)
- Insert an initialization for each new name in the appropriate landing pad
- After each assignment to $i \in IV$, insert an update to the affected new names

Reducing $a \leftarrow i \times c$	
Assignment	Operation to Insert
<i>i</i> ← <i>k</i>	$t_{ixc} \leftarrow t_{kxc}$
i ← - k	$t_{ixc} \leftarrow -t_{kxc}$
$i \leftarrow j + k$	$t_{ixc} \leftarrow t_{jxc} + t_{kxc}$
i ← j - k	$t_{ixc} \leftarrow t_{jxc} - t_{kxc}$

We tested for these four ops on admission to IV

To deal with all of these cases, we build, for $i \in IV$, a set AFFECT(i) that contains every $j \in IV \cup RC$ that can affect the value of i.

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The Cocke-Kennedy Algorithm

Step 5: Computing **AFFECT** Sets

Assumes **SCR**s and **IV** are already available

```
for each i \in IV
    AFFECT(i) \leftarrow \{i\}
for each op o (t \leftarrow o_1 \text{ op } o_2) where t \in IV do
   AFFECT(t) \leftarrow AFFECT(t) \cup \{o_1, o_2\}
changed ← true
while (changed)
   changed ← false
   for each i \in IV
       NEW \leftarrow \bigcup_{o \in AFFECT(i) \cap IV} AFFECT(o)
                                                          Transitive
       if AFFECT(i) \cap NEW \neq \emptyset
                                                          closure
         then changed = true
                                                                                      Applied to
                                                                                       each SCR
      AFFECT(i) \leftarrow AFFECT(i) \cup NEW
```

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The Cocke-Kennedy Algorithm

Step 6: Replacement Assumes all the sets from steps 1 through 5

This step is the heart of the transformation.

CLIST(*y*) is the set of constant multipliers for *y*

Recall that each **CANDIDATE** has the form $(t \leftarrow i \times c, i \in IV, c \in RC)$

```
/* build up a set of multipliers for each variable */
for each x \in IV \cup RC
    CLIST(x) \leftarrow \emptyset
for each op p \in CANDIDATES (t \leftarrow i \times c, i \in IV, c \in RC) do
    for each y \in AFFECT(t)
        CLIST(y) \leftarrow CLIST(y) \cup \{c\}
for each y \in (IV \cup RC) with CLIST(y) \neq \emptyset do
    for each c \in CLIST(y) /* initialize reduced IV */
        T(y,c) \leftarrow a new temporary name
        insert "T(y,c) \leftarrow y \times c"in landing pad
/* insert updates for each reduced IV */
for each op p (t \leftarrow o_1 \circ p \circ o_2) with t \in IV and CLIST(t) \neq \emptyset
  for each c \in CLIST(t)
       insert after p "T(t,c) \leftarrow T(o_1,c) op T(o_2,c)"
/* replace the candidate operations */
for each operation p \in CANDIDATES do
                                                          Applied to
                                                           each scr
    replace p with the operation t \leftarrow T(x,c)
```

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The Cocke-Kennedy Algorithm

Step 7: Linear function-test replacement

```
for each operation o in an SCR if o is a conditional branch (i op k \Rightarrow label) with i \in IV \& k \in RC then select some c \in CLIST(i) /* t_{i \times c} already exists, from Step 6 */ if neither t_{k \times c} or t_{c \times k} exist then insert t_{c \times k} into the hash table of names insert t_{c \times k} \leftarrow c \times k in the landing pad replace the conditional branch with t_{i \times c} op t_{c \times k} \Rightarrow label Applied to each SCR
```

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The Cocke-Kennedy Algorithm

Step 8: Dead Code Elimination

- This algorithm leaves behind a mess
 - ◆ Original induction variables and their updates are still in the code
 - ◆ Shotgun approach to creating reduced induction variables leaves more behind
 - \rightarrow Not all of the $t_{a \times b}$ are actually used
- For the result to be an improvement, it needs some clean up
- Apply a standard dead-code elimination technique
 - ◆ **DEAD** followed by **CLEAN** will do the job
 - ♦ Other algorithms work, too

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The Cocke-Kennedy Algorithm

The Problem

- Easy to apply transformation by hand
- Difficult to automate the process

The Algorithmic Plan

- 1. Find loops in the control-flow graph Entire CFG
- 2. Find region constants for those loops # ops
- 3. Find induction variables # ops
- 4. Find operations that are candidates to be reduced # operations
- 5. Find all the values that affect the uses in candidate operations # values³
- 6. Perform the actual replacement # candidates
- 7. Rewrite end-of-loop tests onto newly introduced induction variables # ops
- 8. Dead-code elimination



Next class

- Vick-Simpson **OSR** algorithm
 - ◆ See K. Cooper, L.T. Simpson, and C. Vick, "Operator Strength Reduction," *ACM Transactions on Programming Languages and Systems (TOPLAS)* 23(5), Sept 2001, pages 603-625.
- Operates over static single assignment form rather than the CFG and individual ops
- Properties of **SSA** let us simplify the algorithm and reduce its costs